

**CLAIMS**

1. (Currently Amended) A system for decoupling a capacitive path from an IO pad and a protected component, comprising:
  - a protected component;
  - an IO pad coupled to the protected component;
  - a source of current to the IO pad;
  - a first circuit directly connected to the IO pad and the protected component, which ceases to conduct after being exposed to a current;
  - a second circuit able to cause the first circuit to cease conducting in response to variations in voltage or current, comprising:
    - a node coupled to the first circuit;
    - first and second diodes coupled to the node; and
    - a capacitive path that is decoupled from the IO pad and protected component in response to the first circuit ceasing ceases to conduct.
2. (Original) The system of Claim 1, wherein the protected component comprises a processor.
3. (Original) The system of Claim 1, wherein the first circuit comprises a fuse.
4. (Currently Amended) The system of Claim 1, wherein the second circuit comprises a fuse blow pad that is coupled to the node.
5. (Cancelled) The system of Claim 1, wherein:

the second circuit comprises a control signal input; and

the second circuit shorts to ground upon receipt of a control signal.

6. (Cancelled) The system of Claim 1, wherein the second circuit comprises a field-effect transistor.
7. (Currently Amended) The system of Claim 1, wherein the capacitive path comprises:
  - [[a]] the node coupled to the first circuit;
  - [[a]] the first diode, the anode of which is coupled to the node; and
  - [[a]] the second diode, the cathode of which is coupled to the node.
8. (Original) The system of Claim 7, wherein the voltage coupled to the cathode of the first diode is a voltage other than a ground voltage.
9. (Original) The system of Claim 7, wherein the voltage coupled to the anode of the second diode is a ground voltage.
10. (Original) The system of Claim 7, wherein:
  - a first voltage is coupled to the IO pad;
  - a second voltage is coupled to the second circuit; and
  - the difference between the first voltage and the second voltage is less than the activation voltage of the first diode or the second diode.

11. (Original) The System of Claim 7, wherein:

the second circuit has a control signal input;

the second circuit shorts to ground upon receipt of a control signal;

a voltage is coupled to the IO pad; and

the difference between the voltage coupled to the IO pad and the ground voltage is less than the activation voltage of the first diode or the second diode.

12. (Original) The system of Claim 11, wherein a plurality of fuse blow control devices are connected to the same fuse blow control signal input.

13. (Cancelled) A system for decoupling a capacitive path from an IO pad and a protected component comprising:

a protected component;

an IO pad coupled to the protected component;

a source of current to the IO pad;

a first circuit which ceases to conduct when exposed to a current;

a second circuit which ceases to conduct when exposed to a current;

a third circuit able to cause the first circuit to cease conducting in response to variations in voltage;

a fourth circuit able to cause the second circuit to cease conducting in response to variations in voltage; and

a capacitive path that is decoupled from the IO pad and protected component when the first and second circuits cease conducting.

14. (Cancelled) The System of Claim 13, wherein the capacitive path comprises a diode pair, further comprising:
- a first node;
  - a first diode, the anode of which is coupled to the first node;
  - a second diode, the cathode of which is coupled to the first node;
  - a second node coupled to the cathode of the first diode; and
  - a third node coupled to the anode of the second diode.
15. (Cancelled) The System of Claim 13, wherein the third circuit comprises a fuse blow pad.
16. (Cancelled) The System of Claim 13, wherein the fourth circuit comprises a fuse blow pad.
17. (Cancelled) The System of Claim 13, wherein the first circuit comprises a fuse.
18. (Cancelled) The System of Claim 13, wherein the second circuit comprises a fuse.
19. (Cancelled) The System of Claim 13, wherein a voltage is coupled to the third circuit.
20. (Cancelled) The System of Claim 13, wherein the voltage coupled to the fourth circuit is a voltage other than ground.
21. (Cancelled) The System of Claim 13, wherein a voltage is coupled to the first circuit.

22. (Cancelled) The System of Claim 13, wherein the voltage coupled to the second circuit is ground.

23. (Cancelled) The system of Claim 13, wherein:

a first voltage is coupled to first circuit;

a second voltage is coupled to the second circuit;

a third voltage is coupled to the third circuit;

a fourth voltage is coupled to the fourth circuit;

the difference of the first voltage and the third voltage causes the first circuit to cease conducting; and

the difference of the second voltage and the fourth voltage causes the second circuit to cease conducting.

24. (Cancelled) The system of Claim 13, further comprising:

a plurality of capacitive paths, IO pads, and protected elements, in which:

a capacitive path is coupled to an IO pad and protected element;

each capacitive path is coupled to the first circuit;

each capacitive path is coupled to the second circuit;

each capacitive path is coupled to the third circuit; and

each capacitive path is coupled to the fourth circuit.

25. (Cancelled) The system of Claim 24, in which:

a capacitive path comprises a diode pair;  
the first node of a diode pair is coupled to an IO pad and a processor;  
the second node of each diode pair is coupled to the first circuit;  
the second node of each diode pair is coupled to the third circuit;  
the third node of each diode pair is coupled to the second circuit; and  
the third node of each diode pair is coupled to the fourth circuit.

26. (Cancelled) A method for decoupling a capacitive path from an IO pad and a protected component, comprising:

applying a first voltage to an IO pad of a protected component;  
generating a current between the IO pad and a control device; and  
separating the IO pad and protected component from a capacitive path as a function of the current between the IO pad and the control device.

27. (Cancelled) A computer program product for decoupling a capacitive path from an IO pad and a protected component, the computer program product having a medium with a computer program embodied thereon, the computer program comprising:

computer code for applying a first voltage to an IO pad of a protected component;  
computer code for generating a current between the IO pad and a control device; and  
computer code for separating the IO pad from a capacitive path as a function of the current between the IO pad and the control device.

28. (Cancelled) A processor product for decoupling a capacitive path from an IO pad and a protected component, the product having a medium with a computer program embodied thereon, the computer program comprising:

computer code for applying a first voltage to an IO pad of a protected component;

computer code for generating a current between the IO pad and a control device; and

computer code for separating the IO pad from a capacitive path as a function of the current between the IO pad and the control device.

29. (Original) The system of Claim 3, wherein the fuse is blown by a laser.

30. (Cancelled) The system of Claim 13, wherein the first circuit has ceased to conduct due to a signal generated by the third circuit, but the second circuit has not ceased to conduct.

31. (New) A method for decoupling a capacitive path from an IO pad and a protected component, comprising:

generating by a current source a current to the IO pad;

directly connecting a first circuit to the IO pad and the protected component, wherein the first circuit ceases to conduct in response to being exposed to a current;

coupling a node of a second circuit to the first circuit;

coupling first and second diodes of the second circuit to the node;

in response to variation in voltage or current, causing, by the second circuit, the first circuit to cease conducting; and

in response to the first circuit ceasing to conduct, decoupling a capacitive path from the IO pad and the protected component.